



## Electrical Engineer / Scientist

### Surface Electronic Warfare Branch

This position reports to the Surface Electronic Warfare Branch within the Tactical Electronic Warfare Division (TEWD) of the US Naval Research Laboratory. TEWD performs advanced research and development covering a range of technical areas that support the development of next-generation EW technologies. Research and development of EW system prototypes to protect surface ships is a primary focus of the branch. The work location is at the Washington DC NRL campus.

### MAJOR DUTIES/POSITION DESCRIPTION

#### Electrical Engineer/Scientist

The Surface Electronic Warfare Branch, within the Tactical Electronic Warfare Division (TEWD) of the US Naval Research Laboratory (NRL) in Washington, DC seeks a highly motivated individual to help lead the research, development, testing and implementation of Electronic Attack (EA) techniques, waveforms, and systems, in support of the surface Navy.

#### Major Duties/Position Description:

The duties of the candidate will include some or all of the following:

- You will help define, lead, and manage research and development projects in EW systems.
- Develop and present research proposals to outside sponsors.
- Lead the development of plans, procedures, and lab setups for the testing of techniques and EW systems against threat systems, through a combination of analysis, modeling, and hardware-in-the-loop simulations.
- Research and development of prototype electronic warfare systems.
- Field testing in support of the above activities.
- Documentation of research and development in the form of formal reports, conferences or journal papers.

**Desired Skills:**

- The ideal candidate will have a selection of the following skills:
- Knowledge of Digital Signal Processing algorithms and implementation
- Knowledge of RF system design, including receiver and transmitter systems
- Expertise with laboratory test equipment, including spectrum analyzers, vector network analyzers, and signal generators
- Experience with FPGA development
- Experience with MATLAB
- Familiarity with C/C++ and/or Python
- Experience in project management and leading teams of engineers/researchers

**MINIMUM REQUIREMENTS**

US Citizenship is required. Dual Citizenship is not permitted.

Ability to obtain and maintain a DoD Security clearance.

Minimum 3.0 GPA.

**JOB BENEFITS**

The Department of the Navy offers a comprehensive benefits package that includes, in part, paid vacation, sick leave, holidays and a 401K-type retirement plan. For additional details visit <http://www.secnave.navy.mil/donhr/Benefits/Pages/Default.aspx>

**NRL Websites**

<https://www.nrl.navy.mil>

<https://netgroup.nrl.navy.mil>

**HOW TO APPLY:** <https://navalresearchlab.app.box.com/f/8562746dcddb4182b1572b6966662d21>

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